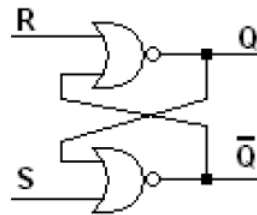


Lab. Script 7

Latch RS

EXERCISE TO DO DURING NON-PRESENTIAL HOURS

- Using the 74LS02 implement in hardware and test the following circuit:



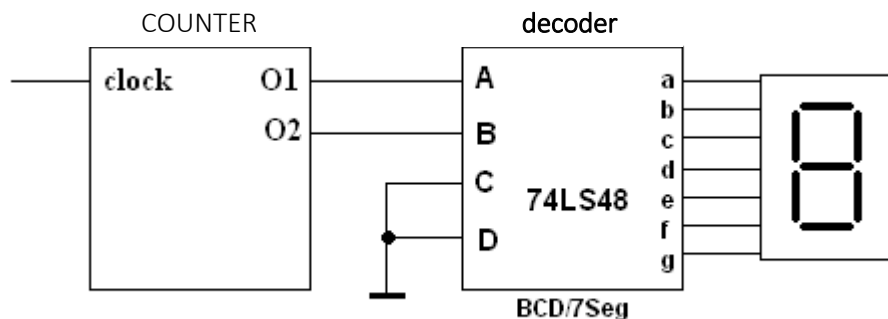
Obtain the values of Q and \bar{Q} to all the possible input combinations.

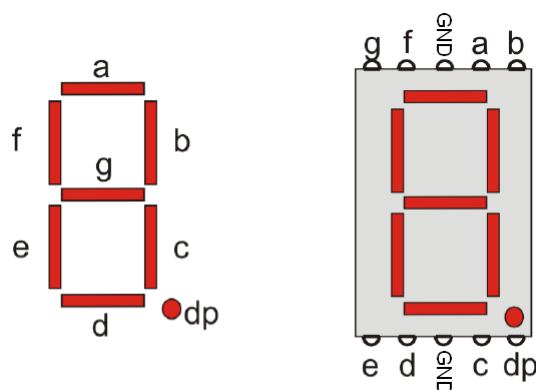
Asynchronous Counters

- Using the 74LS76 (JK flip-flop active to downward transition with low active clear and reset inputs) implement a downward counter that performs the following counting sequence:

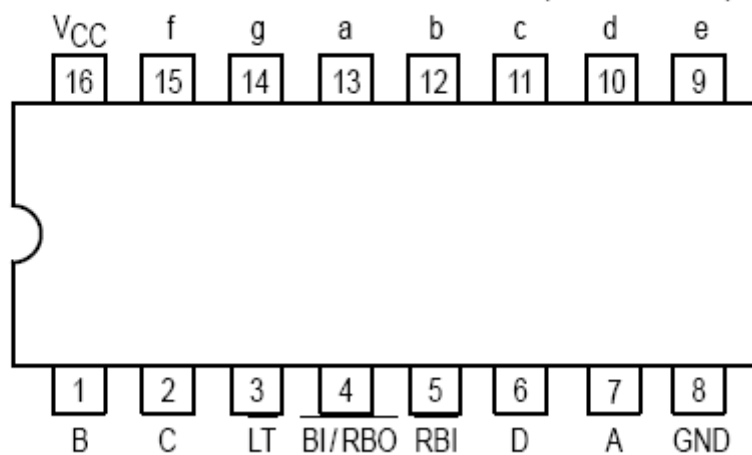
3,2,1,3,2,...

- Interconnect the circuit of the previous exercise to the decoder BCD / 7 Segments as shown in the following figure:





CONNECTION DIAGRAM DIP (TOP VIEW)



74LS48

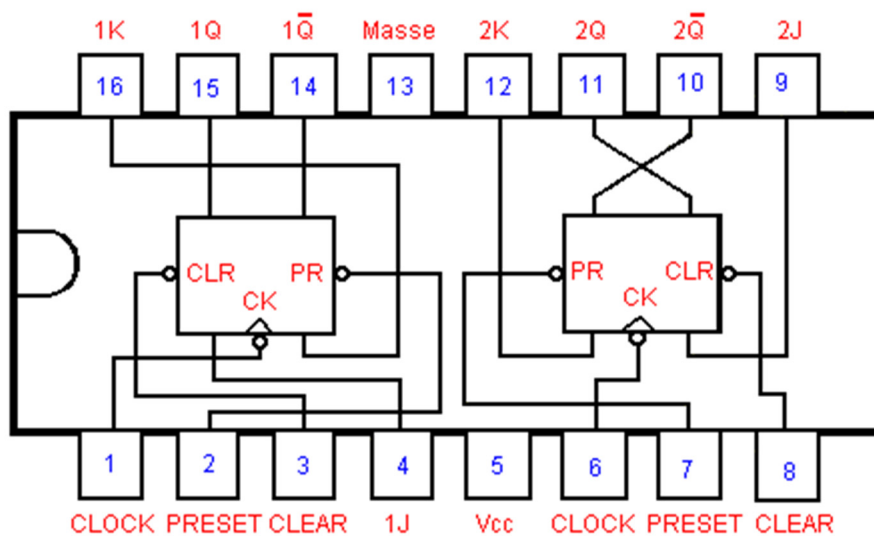


Fig. 55. - Brochage du circuit intégré 74LS76.